

<b>Notice of References Cited</b>	Application/Control No. 10/774,990		Applicant(s)/Patent Under Reexamination AMATANGELO ET AL.	
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#### U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
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#### FOREIGN PATENT DOCUMENTS

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#### NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
*	U	Zhao et al. "Timing-Driven Partitioning and Timing Optimization of Mixed Static-Domino Implementations" IEEE, November 2000.			
	V	Norton et al. "Transistor-level Sizing and Timing Verification of Domino Circuits in the Power PC Microprocessor" IEEE 1997.			
	W				
	X				

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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